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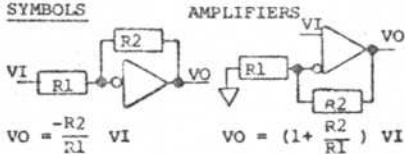
VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DAL(+)

HARDWARE ABBREVIATIONS

MCC MAIN CONTROL CARD
 IFC INTERFACE CARD
 PSC POWER SUPPLY CARD
 SCR THYRISTOR ASSEMBLY
 DGC DIAGNOSTIC CARD
 MFC MOTOR FIELD CONTROL

MDR MODIFICATION RACK

SYMBOLS



$VO = \frac{-R2}{R1} VI$ $VO = (1 + \frac{R2}{R1}) VI$

CASE GROUND

$VO = \text{SIGN} () \times \text{ABSOLUTE VALUE OF VI}$

STAB ON TERMINAL

TERMINAL AT 2TB, 3TB, 4TB, RTB.
 EX: 9 - 2TB9; X2 - RTBX2

TERMINAL AT T.B.'s

POTENTIOMETER ARROWS ON THE CARD
 ELEMENTARY DIAGRAMS INDICATE THE
 WIPER DIRECTION AS THE POTENTIOMETER
 SHAFT IS ROTATED CLOCKWISE TO INCREASE
 FUNCTION.

THESE RESISTORS ARE CRIMPED IN WIRE
 HARNESS.

FUNCTION	USE	LOC	JUMPERS
60HZ		MCC AA-AS, BA-BB, CA-CS	
		MFC ZA-ZB (IF USED)	
50HZ	X	MCC AA-AF, BA-BF, CA-CF	
IOC-400%	X		NONE
-500%		IFC I-1HI	
-300%		IFC I-1LO	
SR5 - 9v			(NONE)
9 - 20v	X	MCC SRH-COM	
JOGR 10v			(NONE)
20v		MCC JH - COM	
LT. 3-7sec	X		(NONE)
2 - 60sec		MCC 3320FROM LTI TO COM	
VREG		IFC NT-CEMF, CC-COM	
DC TACHO	X		(NONE)
AC TACHO		MCC AT1-AT2	
TACHO FILT		IFC TC-TC	
TACHO V.			
24-64vac		IFC NT-NT1, PT-PT1	
27-71vac		IFC NT-NT1, PT-PT1	
60-160vac		IFC NT-NT2, PT-PT2	
66-177vac		IFC NT-NT2, PT-PT2	
110-300vac	X	IFC NT-NT3, PT-PT3	
120-300vac		IFC NT-NT3, PT-PT3	
G134 G256			
MFC 1.7		MFC NONE	
MFC 1.3		MFC YB-YD	
MFC 2.4	X	MFC YA-YB	
MFC 4.0		MFC YA-YB, YC-YD	
MFC 7.0		MFC YA-YC	
MFC 11.25		MFC YA-YC, YB-YD	
L/R < .25S		MFC QA-QB	
INH RUN		DGC D1-D2 (IF USED)	

SIGNAL DEFINITIONS AND LOCATIONS

- * CEMF COUNTER EMF (3 16)
- * CFB CURRENT FEEDBACK (3 16)
- CMFA ABSOLUTE VALUE CEMF (3 08)
- CRM CROSSOVER MODIFY (4 11)
- DFP DELAYED FIRING POWER (3 25)
- * DR DRIVER REFERENCE (3 33)
- * EAO ERROR AMP OUTPUT (3 33)
- EST EXTERNAL FLT STOP INPUT (3 14)
- FALT FAULT (3 14)
- * FC FIELD CURRENT (NS26)
- FDR FIELD DIAGNOSTIC REFERENCE (08)
- FEA FIELD ECONOMY ADJUST (4 25)
- FF FIELD FAULT (NS28)
- IABS MOTOR CURRENT ABSOLUTE (3 09)
- ILA CURRENT LIMIT ADJUST (3 23)
- IMET CURRENT SIGNAL FOR METER (3 10)
- * IPU INITIAL PULSE (3 20)
- * LR LOCAL REF. FROM DGC (3 33)
- * JOG JOG SWITCH INPUT (3 23)
- * JOGR JOG REFERENCE INPUT (3 31)
- * MAC MAX/MA CONTROL SIGNAL (3 20)
- MSW MODE SWITCH (3 30)
- * OSC OSCILLATOR (3 17)
- * PCR PHASE CONTROL REF. (3 26)
- * PRE DRIVE PRECONDITION (3 21)
- * ØSEQ PHASE SEQUENCE (3 14)
- RERR REGULATOR ERROR (3 27)
- RIJ INTEGRATOR SUMMING JUNCTION (3 27)
- RJ REGULATOR SUMMING JUNCTION (3 31)
- RRA REGULATOR RESPONSE ADJUST (3 30)
- RSET RESET (3 16)
- * RTR READY TO RUN (3 16)
- * RUN RUN SWITCH INPUT (3 21)
- * SA-C PHASE SYN OUTPUT (3 16)
- * SFB SPEED FEEDBACK (3 20)
- SMET SPEED SIGNAL FOR METER (3 12)
- * SR SYSTEM REFERENCE INPUT (3 29)
- * SYS SYSTEM FAULT TRIP (3 13)
- * TA OUTPUT FOR TACHO TRIP ADJUST (3 20)
- TF TACHO FAULT (NS28)
- * TFB TACHOMETER FEEDBACK (3 20)
- TFR AC TACHO FREQUENCY OUTPUT (3 13)
- * TR TIMED REFERENCE (3 33)
- * VFB VOLTAGE FEEDBACK (3 19)
- * WFR WEAK FIELD REFERENCE (3 20)

(* - TEST POINT ON DOOR FRONT)

MAPPING SYSTEM

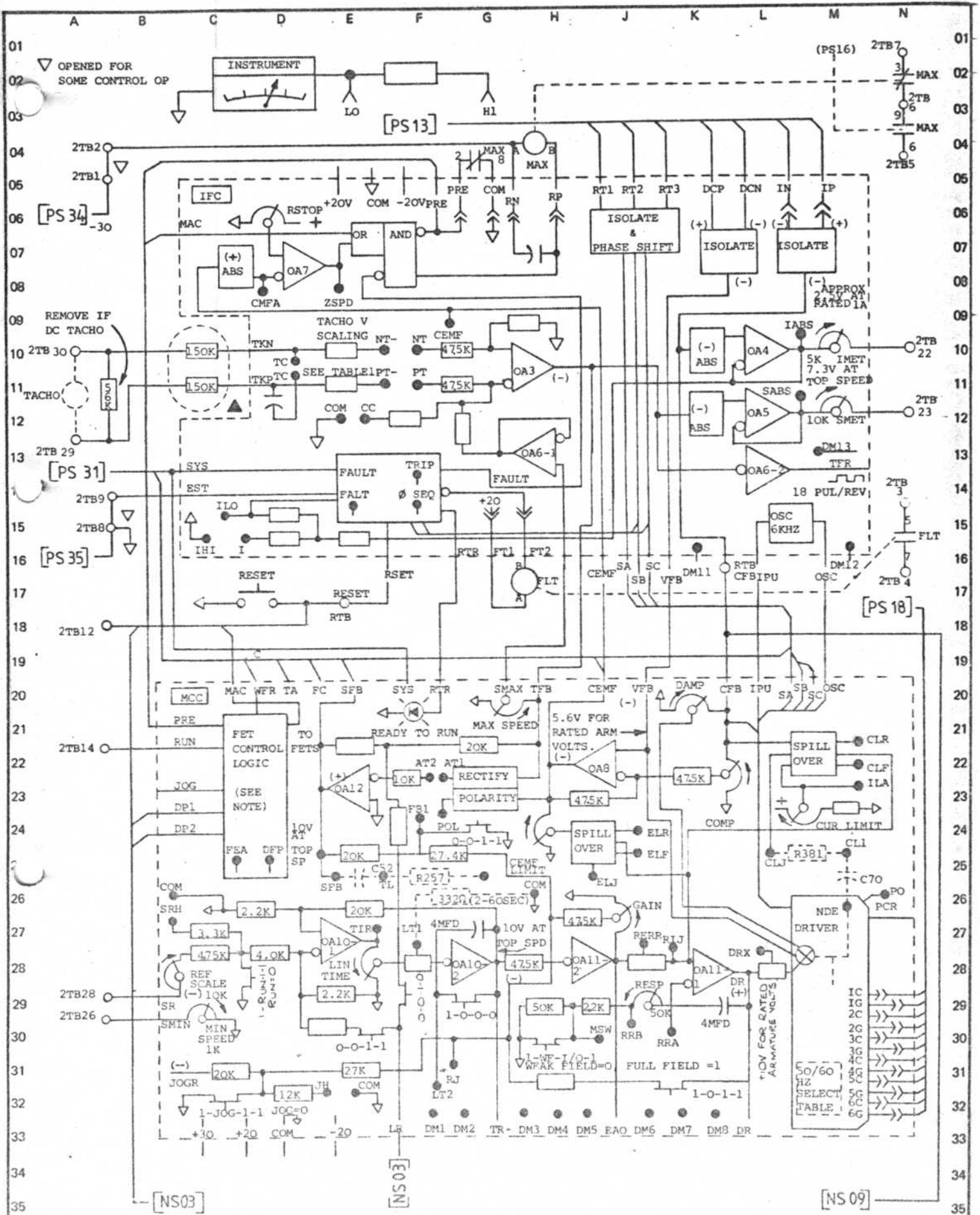
(NS/PS/TS) PS - PAST SHEET
 NS - NEXT SHEET
 TS - THIS SHEET

NOTE: FIELD EFFECT TRANSISTOR: THE
 CLOSED/OPEN (I/O) STATE OF THESE
 SWITCHED FOR "PRECONDITION" - "RUN"
 OR JOG" - "DIAGNOSTIC STATIC" -
 "DIAGNOSTIC RUN" IS SHOWN BY A
 FOUR DIGIT WORD WITH STATE SEQUENCE.

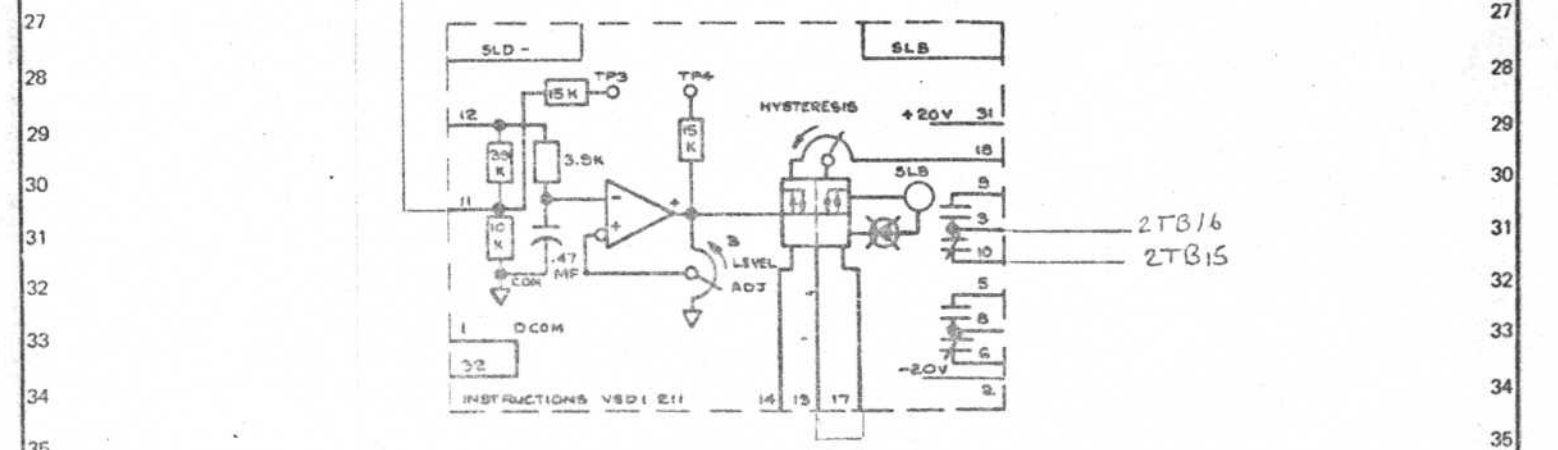
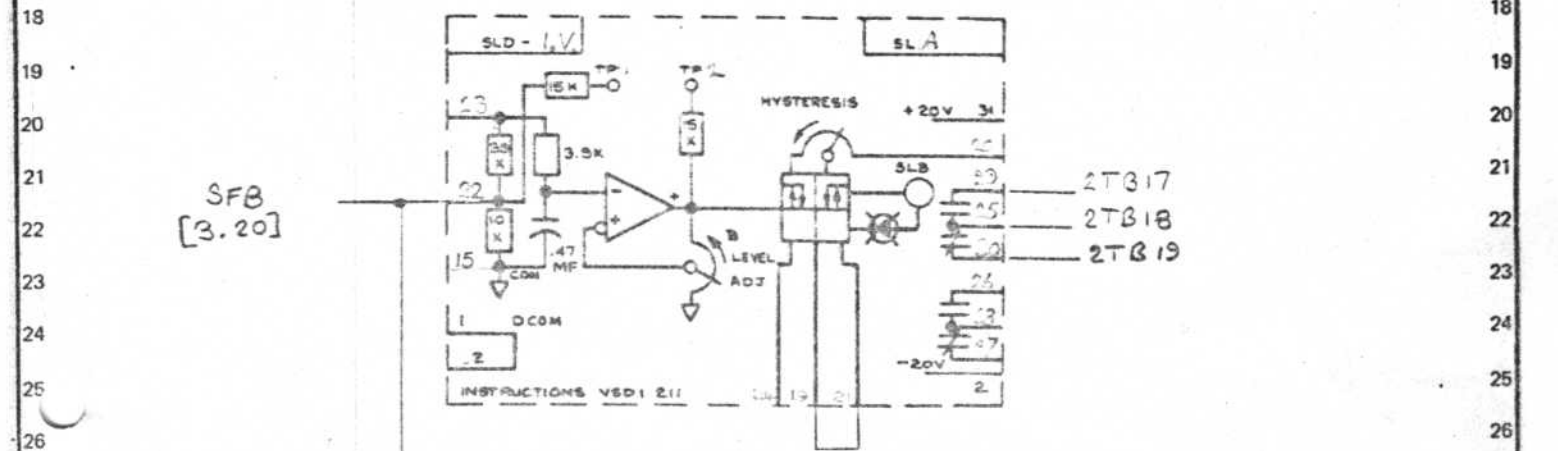
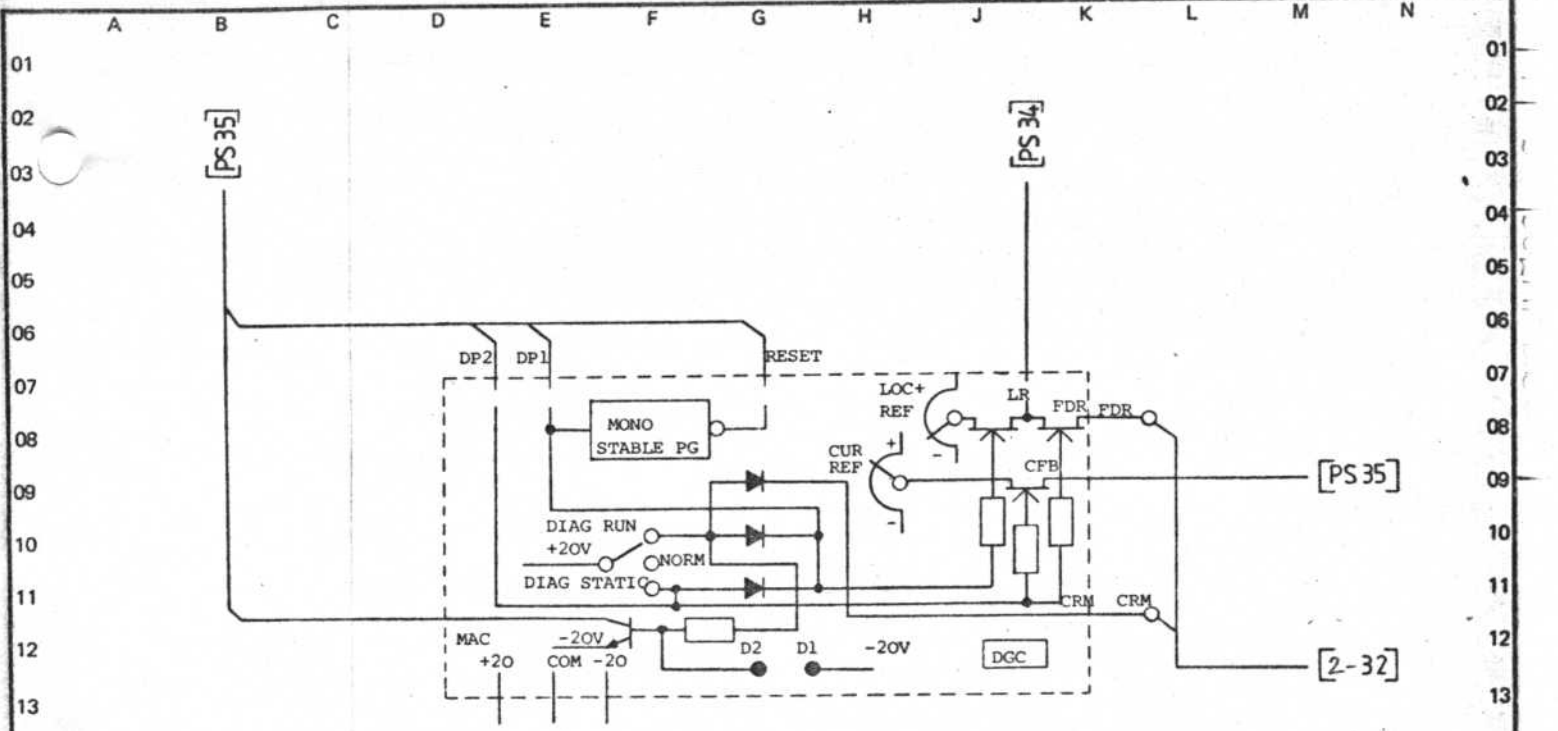
DR SH CONTD. 2 ELEMENTARY DIAGRAM 902M122BC

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CHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE			BDC 3034 R 10HP TI MATRIX		IDENT
			SEE SHEET	FRS		16-7-79					DR SH
			7.	F.R.S			VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.		GO NUMBER	ELEMENTARY DIAGRAM	CONTD.
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TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	16 7 79	 Simplex VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	10 HP BDC 2034 R			IDENT
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