

A B C D E F G H J K L M N

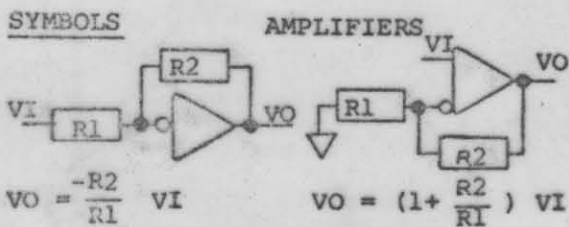
VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DAI(+)

## HARDWARE ABBREVIATIONS

MCC MAIN CONTROL CARD  
 IFC INTERFACE CARD  
 PSC POWER SUPPLY CARD  
 SCR THYRISTOR ASSEMBLY  
 DGC DIAGNOSTIC CARD  
 MFC MOTOR FIELD CONTROL

MDR MODIFICATION RACK

## SYMBOLS



CASE GROUND

VO = SIGN ( ) X ABSOLUTE VALUE OF VI  
 STAB ON TERMINAL

TERMINAL AT 2TB, 3TB, 4TB, RTB.  
 EX: 9 [2] - 2TB9; X2 [8] - RTB2

TERMINAL AT T.B.'s

POTENTIOMETER ARROWS ON THE CARD  
 ELEMENTARY DIAGRAMS INDICATE THE  
 WIPER DIRECTION AS THE POTENTIOMETER  
 SHAFT IS ROTATED CLOCKWISE TO INCREASE  
 FUNCTION.

FUNCTION	USE	LOC	JUMPERS
60HZ		MCC	AA-AS, BA-BG, CA-CG
		MFC	ZA-ZB (IF USED)
50HZ	X	IFC	0.0047uF RT1-RT2
		IFC	0.0047uF RT2-RT3
		IFC	0.0047uF RT3-RT1
		MCC	AA-AF, BA-BF, CA-CF
IOC-400%			NONE
-500%		IFC	I-IH1
-300%		IFC	I-ILO
SR5 - 9v			(NONE)
9 - 20v		MCC	SRH-COM
JOG 10v			(NONE)
20v		MCC	JH - COM
LT. 3-7sec.			(NONE)
2 - 60sec	X	MCC	332Ω FROM LTITOCOM
VREG		IFC	NT-CEMF, CC-COM
DC TACHO	X		(NONE)
AC TACHO		MCC	AT1-AT2
TACHO FILT		IFC	TC-TC
TACHO V.			
24-64vdc		IFC	NT-NT1, PT-PT1
27-71vac		IFC	NT-NT1, PT-PT1
60-160vdc		IFC	NT-NT2, PT-PT2
66-177vac		IFC	NT-NT2, PT-PT2
110-300vdc	X	IFC	NT-NT3, PT-PT3
120-300vac		IFC	NT-NT3, PT-PT3
G134 G256			
1.7		MFC	NONE
2.8		MFC	YB-YD
5.0		MFC	YA-YB
8.0		MFC	YA-YB, YC-YD
13		MFC	YA-YC
25	X	MFC	YA-YC, YB-YD
L/R < .25S	X	MFC	QA-QB
INH RUN		DGC	D1-D2 (IF USED)

SEE NOTE PAGE 9

## SIGNAL DEFINITIONS AND LOCATIONS

\* CEMF COUNTER EMF (316)  
 \* CFB CURRENT FEEDBACK (316)  
 CMFA ABSOLUTE VALUE CEMF (308)  
 CRM CROSSOVER MODIFY (311)  
 DFP DELAYED FIRING POWER (325)  
 \* DR DRIVER REFERENCE (333)  
 \* EAO ERROR AMP OUTPUT (333)  
 EST EXTERNAL FLT STOP INPUT (314)  
 FALT FAULT (314)  
 \* FC FIELD CURRENT (NS26)  
 FDR FIELD DIAGNOSTIC REFERENCE (308)  
 FEA FIELD ECONOMY ADJUST (325)  
 FF FIELD FAULT (328)  
 IABS MOTOR CURRENT ABSOLUTE (309)  
 ILA CURRENT LIMIT ADJUST (323)  
 IMET CURRENT SIGNAL FOR METER (310)  
 \* IPU INITIAL PULSE (320)  
 \* LR LOCAL REF. FROM DGC (333)  
 \* JOG JOG SWITCH INPUT (323)  
 \* JOGR JOG REFERENCE INPUT (331)  
 \* MAC MAX/MA CONTROL SIGNAL (320)  
 MSW MODE SWITCH (330)  
 \* OSC OSCILLATOR (317)  
 \* PCR PHASE CONTROL REF. (326)  
 \* PRE DRIVE PRECONDITION (321)  
 ØSEQ PHASE SEQUENCE (314)  
 RERR REGULATOR ERROR (327)  
 RIJ INTEGRATOR SUMMING JUNCTION (327)  
 RJ REGULATOR SUMMING JUNCTION (331)  
 RRA REGULATOR RESPONSE ADJUST (330)  
 RSET RESET (316)  
 \* RTR READY TO RUN (316)  
 \* RUN RUN SWITCH INPUT (321)  
 \* SA-C PHASE SYN OUTPUT (316)  
 \* SFB SPEED FEEDBACK (320)  
 SMET SPEED SIGNAL FOR METER (312)  
 \* SR SYSTEM REFERENCE INPUT (329)  
 \* SYS SYSTEM FAULT TRIP (313)  
 \* TA OUTPUT FOR TACHO TRIP ADJUST (220)  
 TF TACHO FAULT (NS28)  
 \* TFB TACHOMETER FEEDBACK (320)  
 TFR AC TACHO FREQUENCY OUTPUT (313)  
 \* TR TIMED REFERENCE (333)  
 \* VFB VOLTAGE FEEDBACK (319)  
 \* WFR WEAK FIELD REFERENCE (320)

(\* - TEST POINT ON DOOR FRONT)

## MAPPING SYSTEM

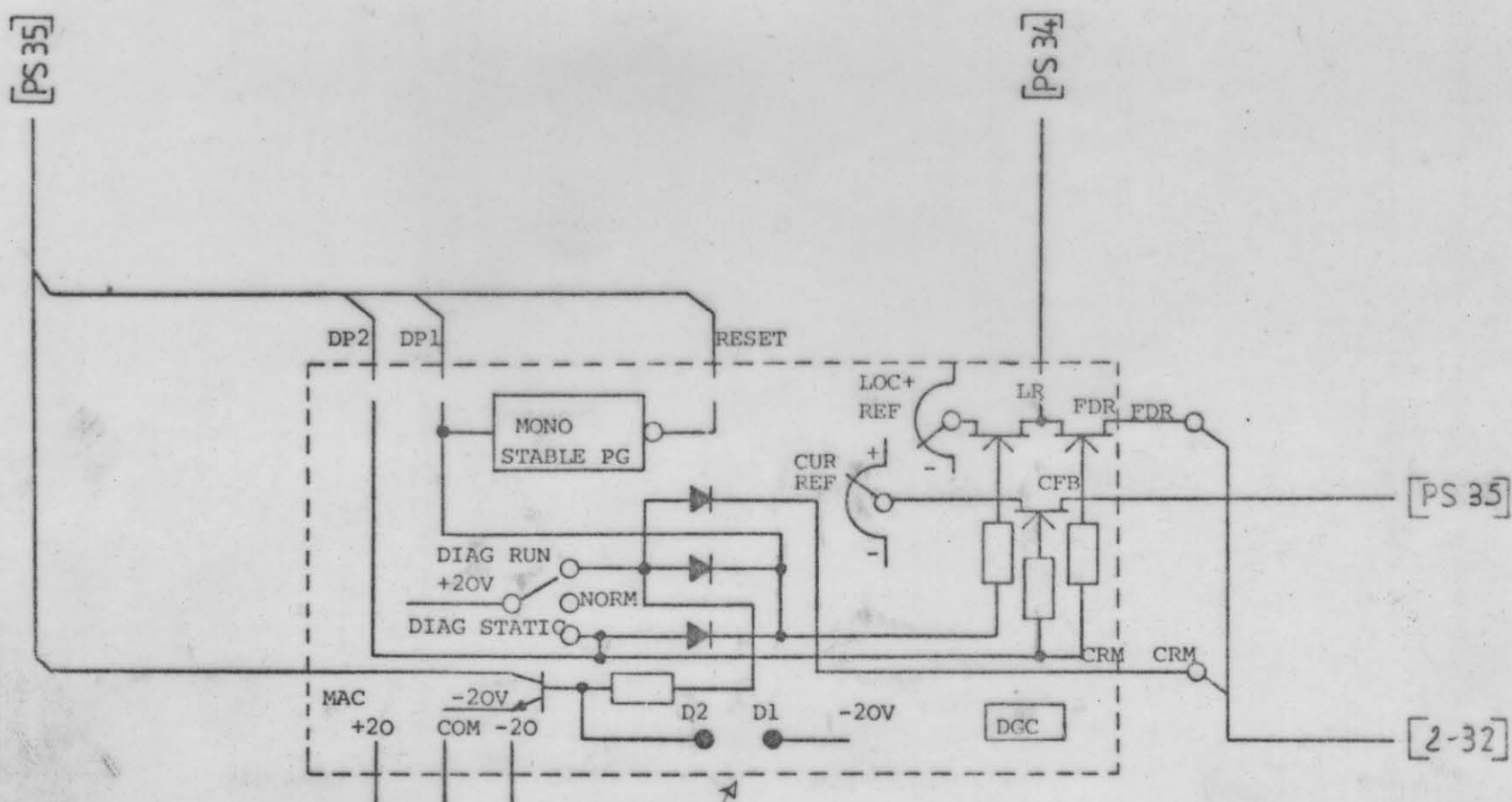
(NS/PS/TS) PS - PAST SHEET  
 NS - NEXT SHEET  
 TS - THIS SHEET

HENCE [PS - 12] DENOTES LOCATION ON PAST SHEET LINE 12. OTHER LOCATIONS ARE  
 DENOTED BY SHEET NUMBER AND LINE, E.G. [1A16] SIGNIFIES LOCATION ON SHEET  
 1A, LINE 16 ETC.

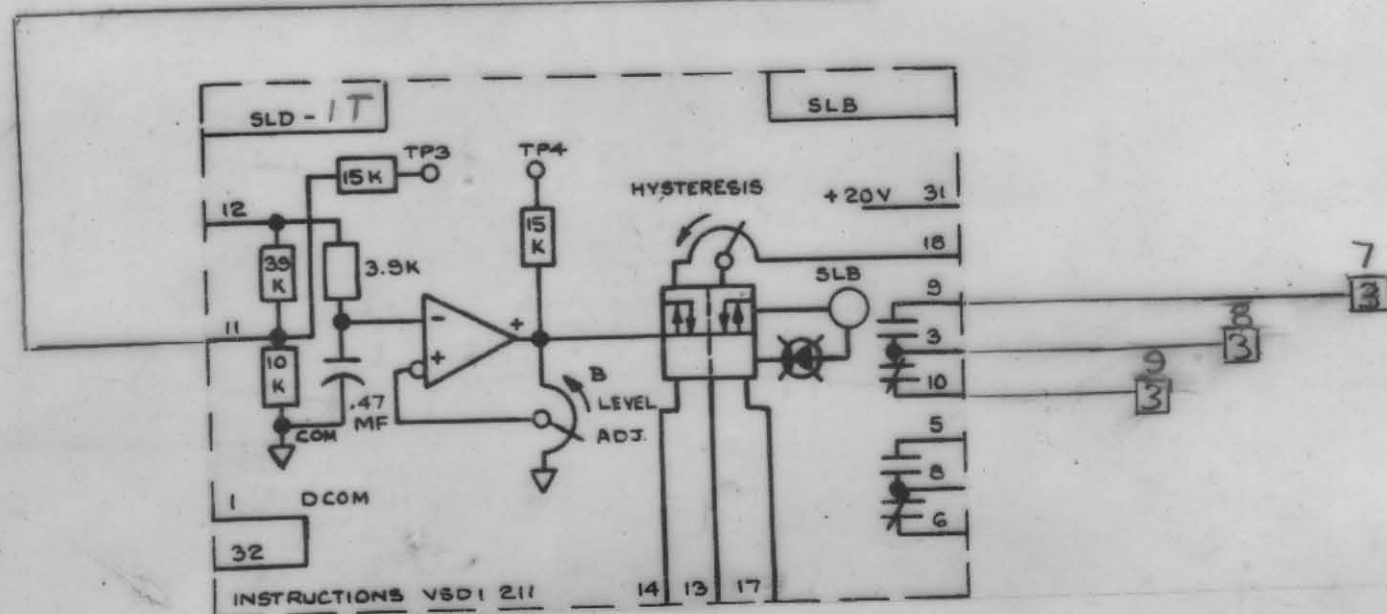
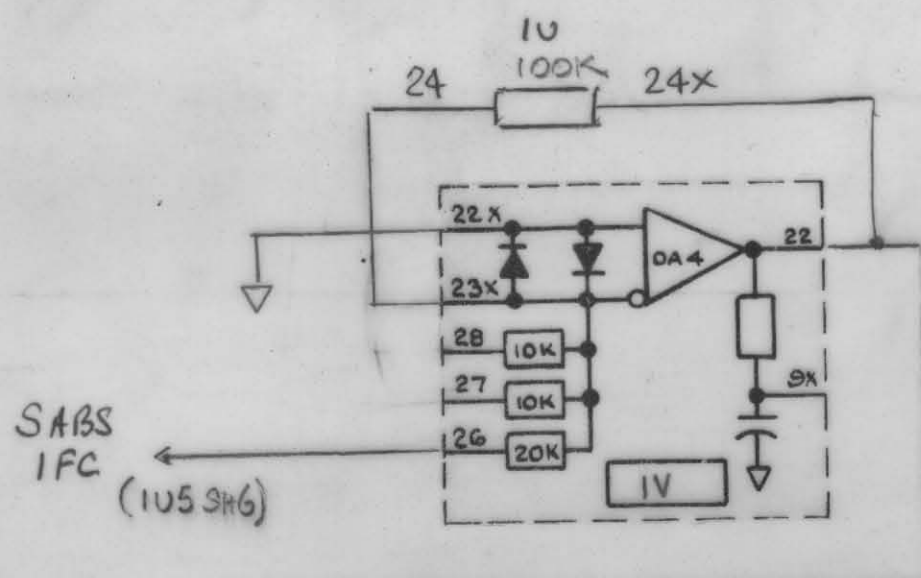
NOTE: FIELD EFFECT TRANSISTOR: THE  
 CLOSED/OPEN (I/O) STATE OF THESE  
 SWITCHED FOR "PRECONDITION" - "RUN"  
 OR JOG" - "DIAGNOSTIC STATIC" -  
 "DIAGNOSTIC RUN" IS SHOWN BY A  
 FOUR DIGIT WORD WITH STATE SEQUENCE.

THESE RESISTORS ARE CRIMPED IN WIRE HARNESS

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	3.9.82	 <b>Simplex</b> VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	BDC 3034R 40/60HP			IDENT	 DR SH
AS SHIPPED						TECHN.			FOR CONE BLANCHARD M/CCO				
22.11.82						ENG.			GO NUMBER	ELEMENTARY DIAGRAM	CONTO.		
						APPD.		233N00	902M126RB	2	1		



LINK D1-D2  
IF DIAGNOSTIC RUN NOT REQUIRED

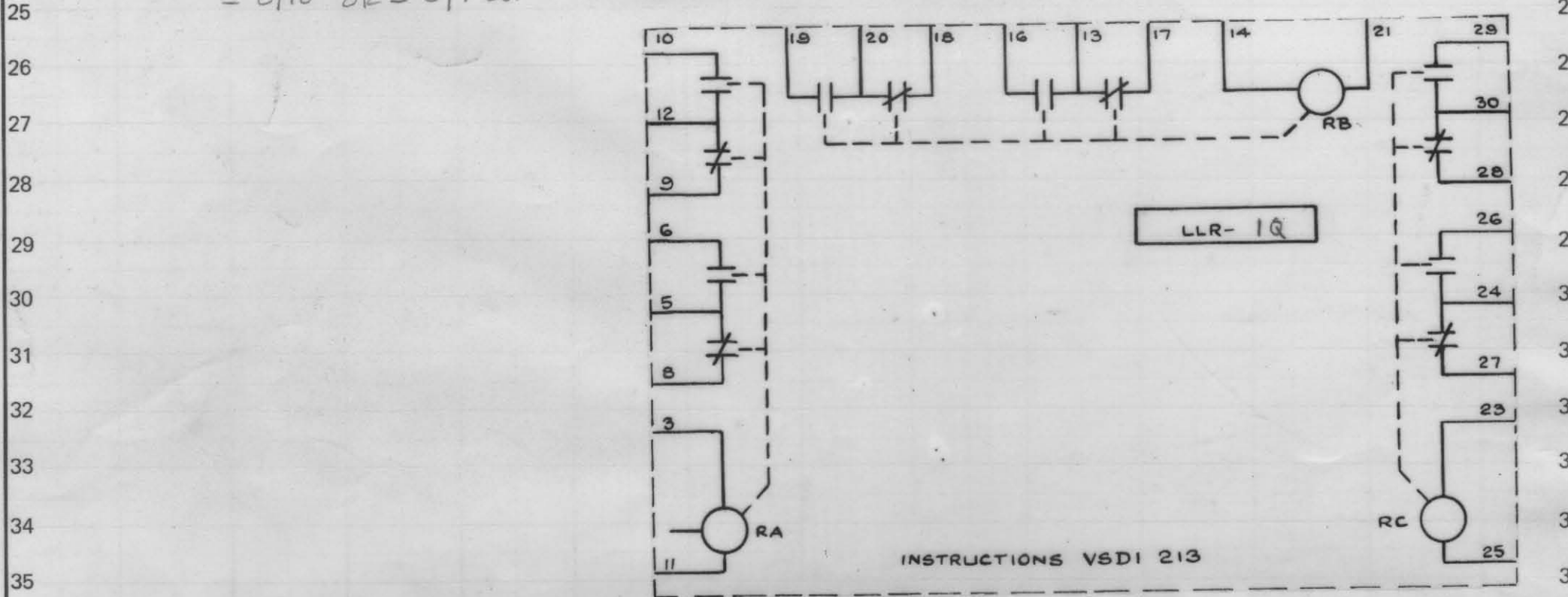
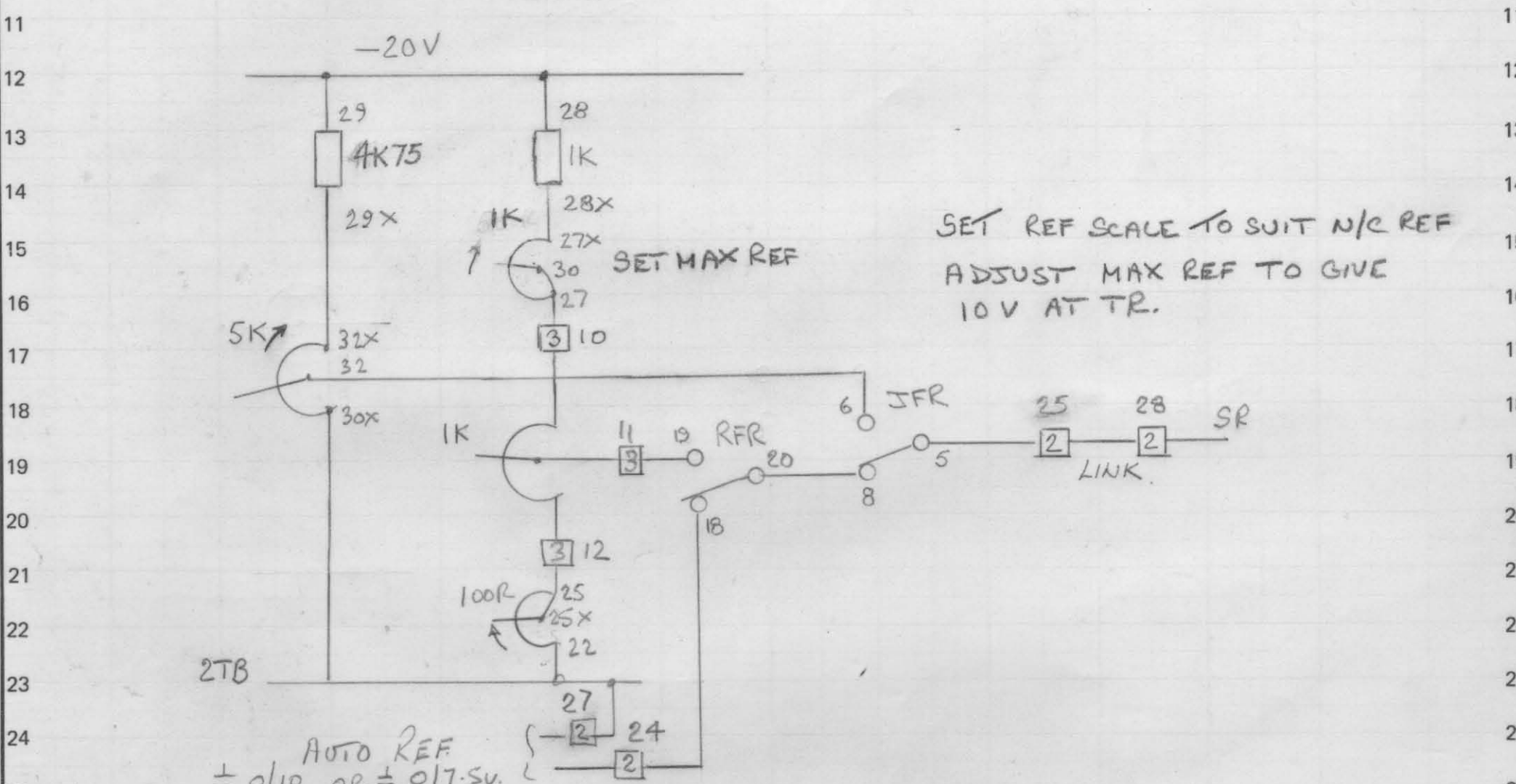
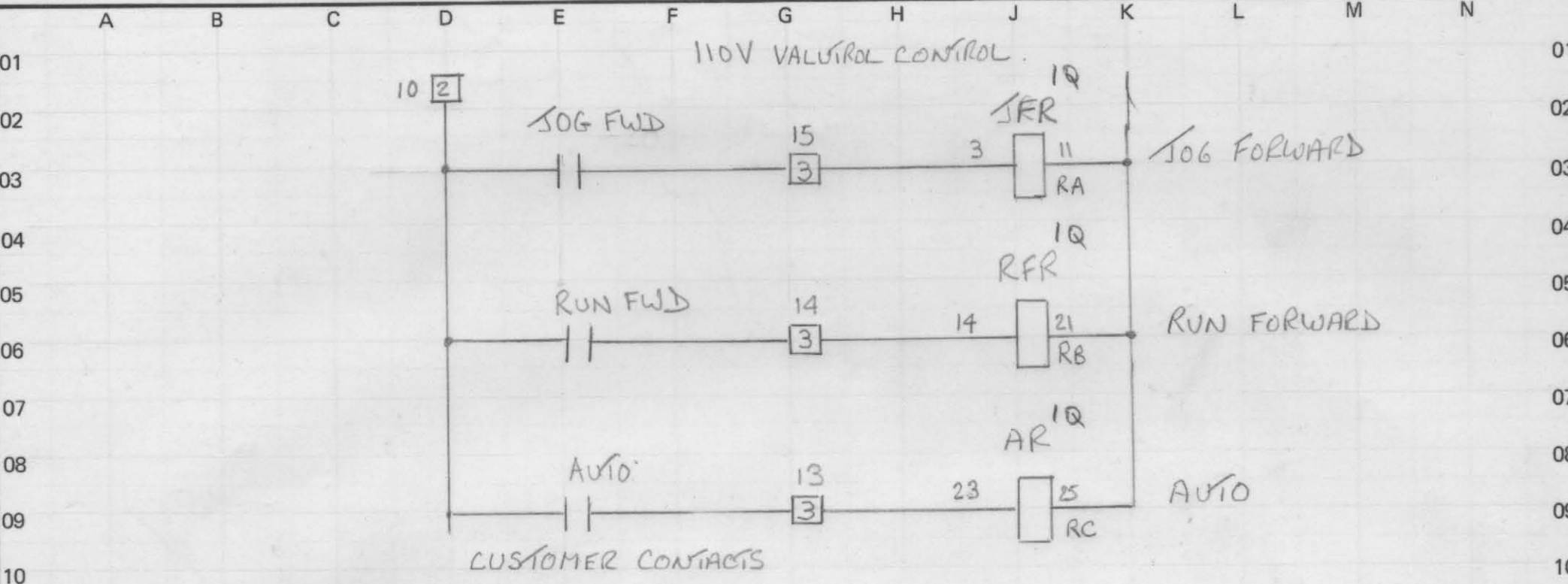


SLOW SPEED DETECTOR

TO OPERATE BETWEEN  
20 - 60 RPM  
SET AT 40 RPM

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	ALLEN WEST		BDC 3034R 40/60 HP		IDENT	
						3.9.82	Simplex				DR SH	
							VARIABLE SPEED DRIVES OPERATION. BRIGHTON, ENGLAND.		GO NUMBER 233N00	ELEMENTARY DIAGRAM 902M126RB	CONTD. 5	4



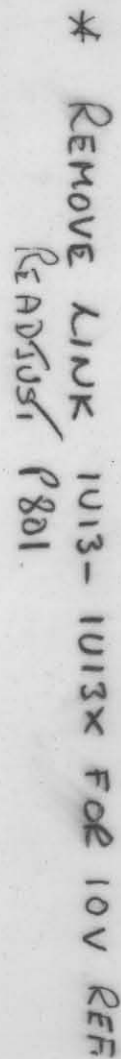


TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	3.9.82	 <b>Simplex</b> VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	BDC 3034R 40/60HP REFERENCE CIRCUITS		IDENT	 DR SH 5
			TECHN.						GO NUMBER	ELEMENTARY DIAGRAM	CONTD.	
			ENG.						233N00	902M126RB	6	

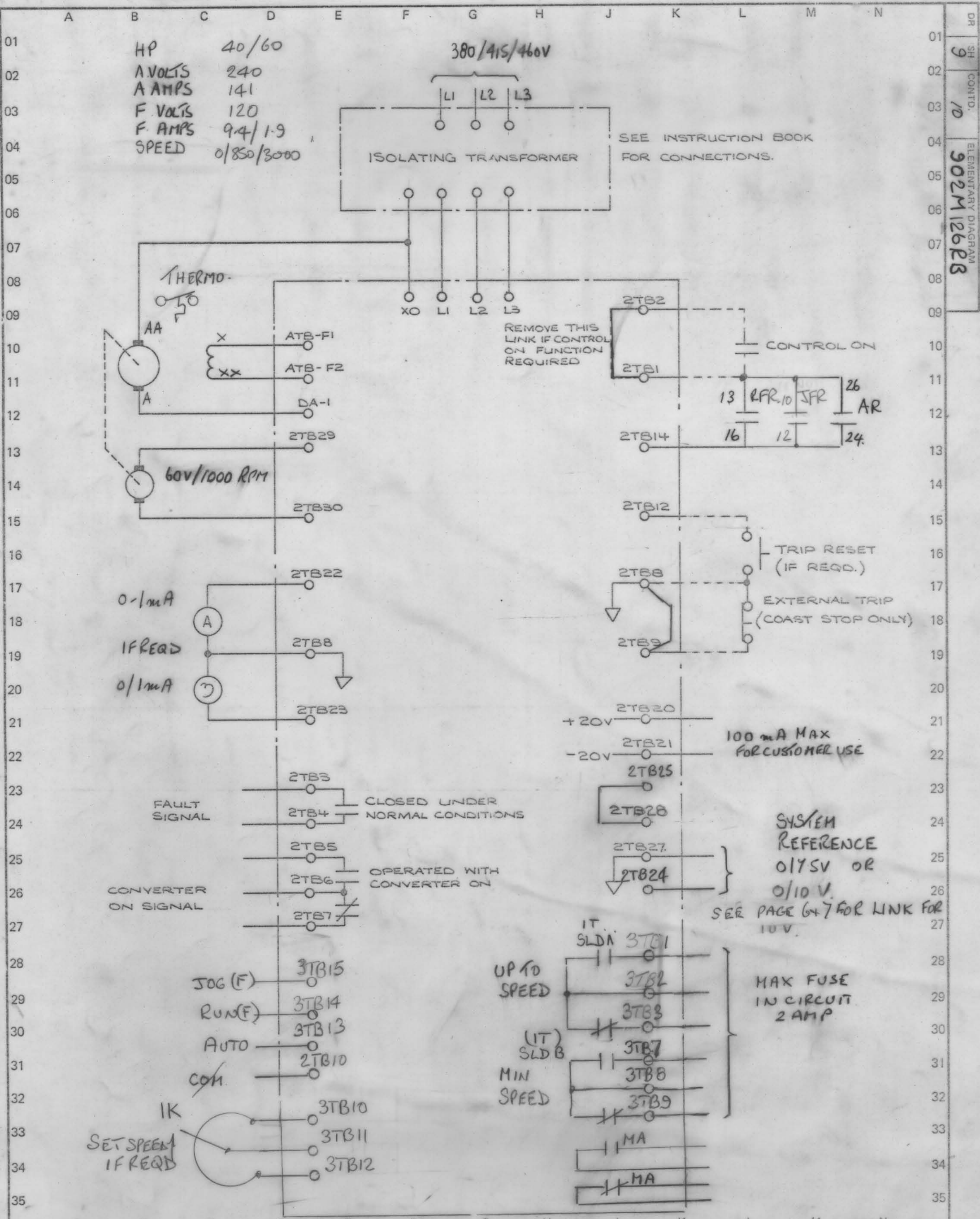
1K POT WAS 500R

22.11.82

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TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	3.9.82	ALLENWEST	BDC 3034R 40/60HP	IDENT	DR	SH
						TECHN.		Simplex	CUSTOMER CONNECTIONS			
						ENG.		VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	GO NUMBER	ELEMENTARY DIAGRAM	CONTD.	
						APPD.			233N00	902M126RB	70	9



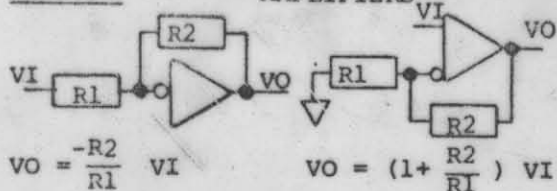
VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DA1(+)

## HARDWARE ABBREVIATIONS

MCC MAIN CONTROL CARD  
 IFC INTERFACE CARD  
 PSC POWER SUPPLY CARD  
 SCR THYRISTOR ASSEMBLY  
 DGC DIAGNOSTIC CARD  
 MFC MOTOR FIELD CONTROL  
 MFE MOTOR FIELD EXCITER  
 MDR MODIFICATION RACK  
 ACC AUXILIARY CONTROL CARD

## SYMBOLS

## AMPLIFIERS



CASE GROUND  
 $VO = \text{SIGN} () \times \text{ABSOLUTE VALUE OF } VI$   
 STAB ON TERMINAL

TERMINAL AT 2TB, 3TB, 4TB, RTB.  
 EX: 9 - 2TB9; X2 - RTBX2

TERMINAL AT T.B.'s

POTENTIOMETER ARROWS ON THE CARD  
 ELEMENTARY DIAGRAMS INDICATE THE  
 WIPER DIRECTION AS THE POTENTIOMETER  
 SHAFT IS ROTATED CLOCKWISE TO INCREASE  
 FUNCTION.

THESE RESISTORS ARE CRIMPED IN WIRE  
 HARNESS.

FUNCTION	USE	LOC	JUMPERS
60HZ		MFC	ZA-ZB (IF USED)
50HZ	X	MCC	HZA - PHA
IOC-400%	X		(NONE)
-500%		IFC	I - IHI
-300%		IFC	I - ILO
SR5 - 9v	X		(NONE)
9 - 20v		MCC	SRH - COM
JOGR 10v			(NONE)
20v		MCC	JH - COM
LT.3-7sec.			(NONE)
2 - 60sec	X		332Ω FROM LTI TO COM
VREG			NT-CMF CC-COM
DC TACHO			(NONE)
AC TACHO		MCC	AT1 - AT2
TACHO FILT		IFC	TC - TC
TACHO V.			
24-64vdc		IFC	NT-NT1 PT - PT1
27-71vac		IFC	NT-NT1 PT - PT1
60-160vdc		IFC	NT-NT2 PT - PT2
66-177vac		IFC	NT-NT2 PT - PT2
110-300vdc	X	IFC	NT-NT3 PT - PT3
120-300vac		IFC	NT-NT3 PT - PT3
G134 G256		IFC	MFC OR MFE
1.8 1.7		ME	NONE
1.3 2.8		ME	YB - YD
2.4 5.0		ME	YA - YB
4.0 8.0		ME	YA-YB, YC-YD
7.0 13		ME	YA - YC
13 25		ME	YA-YC, YB-YD
L/R < .25S		MFC	QA - QB
INH RUN		DGC	D1-D2 (IF USED)
INH DRV CL		MCC	DC1 - COM
FUSELESS		ACC	CFY - CFX

## SIGNAL DEFINITIONS AND LOCATIONS

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 \* CFB CURRENT FEEDBACK (316)  
 CMFA ABSOLUTE VALUE CEMP (308)  
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 \* DR DRIVER REFERENCE (333)  
 \* EAO ERROR AMP OUTPUT (333)  
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 FALT FAULT (314)  
 \* FC FIELD CURRENT (NS26)  
 FDR FIELD DIAGNOSTIC REFERENCE (408)  
 FEA FIELD ECONOMY ADJUST (325)  
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 IMET CURRENT SIGNAL FOR METER (310)  
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( \* - TEST POINT ON DOOR FRONT)

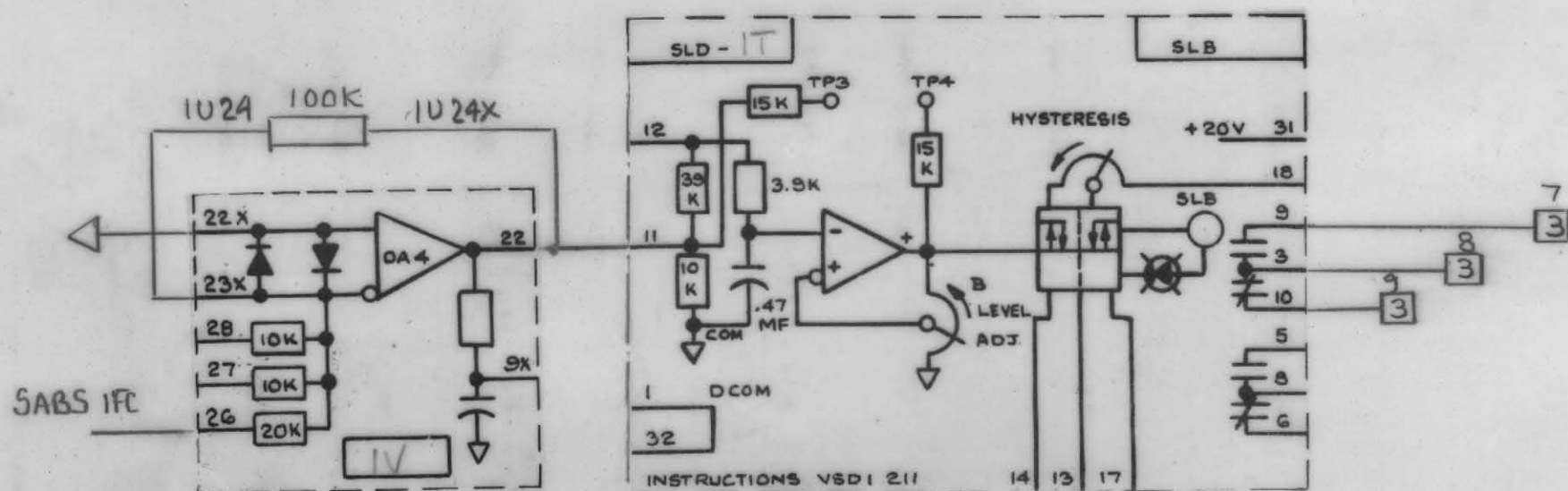
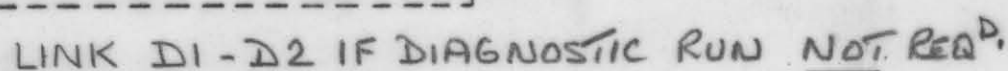
## MAPPING SYSTEM


(NS/PS/TS) PS - PAST SHEET  
 NS - NEXT SHEET  
 TS - THIS SHEET

HENCE (PS - 12) DENOTES LOCATION ON PAST SHEET LINE 12. OTHER LOCATIONS ARE  
 DENOTED BY SHEET NUMBER AND LINE? E.G. (1A16) SIGNIFIES LOCATION ON SHEET  
 1A, LINE 16 ETC.

NOTE: FIELD EFFECT TRANSISTOR: THE  
 CLOSED/OPEN (I/O) STATE OF THESE  
 SWITCHED FOR "PRECONDITION" - "RUN"  
 OR JOG" - "DIAGNOSTIC STATIC" -  
 "DIAGNOSTIC RUN" IS SHOWN BY A  
 FOUR DIGIT WORD WITH STATE SEQUENCE.

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	16.3.83	BDC 3064R 75HP		IDENT		
							TECHN.	(CONE BLANCHARD)		DR SH		
							ENG.					
							APPD.	308N00		1		
							VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.		ELEMENTARY DIAGRAM		CONTD.	
									902M126XC		2	

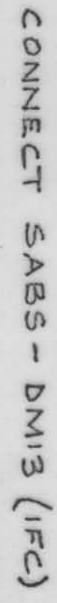


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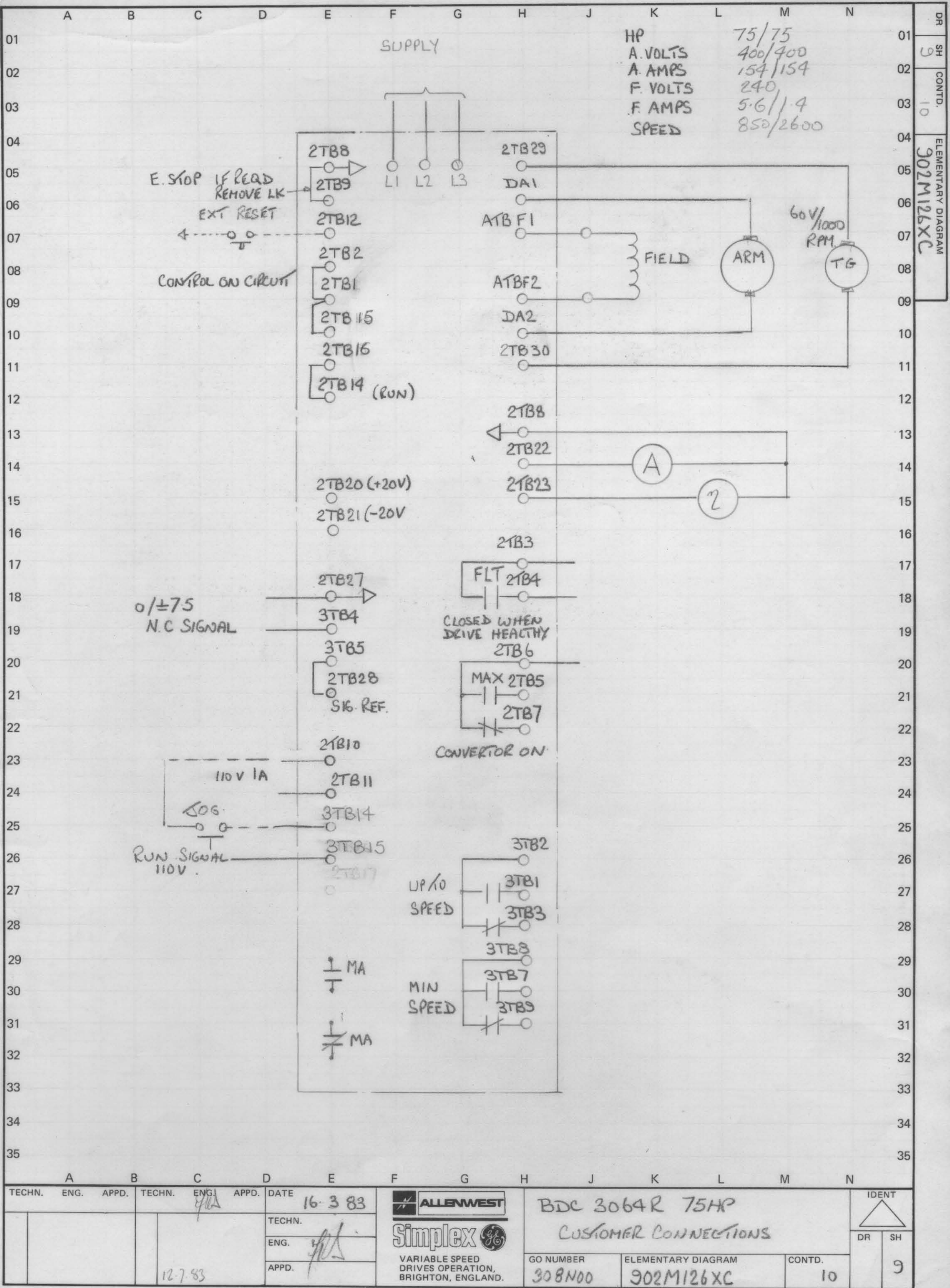






11 0.5V WHEN  
DRIVE UP TO SPEED

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TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	16-3-83	ALLENWEST	BDC 3064R 75HP			IDENT	
						TECHN.		Simplex	CUSTOMER CONNECTIONS			DR SH	
						ENG.		VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	GO NUMBER	ELEMENTARY DIAGRAM	CONTD.		
						APPD.			308N00	902M126XC	10	9	

12-7-83

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